

82E242 - PARAMETRIC EQUALISER AND FILTER EDGE CONNECTOR

From: 82E242

To: 611G Mother Board

No	Description	No	Description
1	HF Shelf/Bell Switch	34	LF Freq. pot wiper a
2	HF Shelf/Bell Switch	—	LF Shelf/Bell Sw 4
3	HF Gain pot ACW	35	LF Freq. pot CW b
4	HF Gain pot CW	36	LF Freq pot wiper b
5	HF Gain pot wiper	37	LF Shelf/Bell Sw 4
—	HF Freq. pot CW a	38	LF Shelf/Bell Sw 3
6	HF Freq. pot CW b	39	LF Shelf/Bell Sw 3
7	Spare	40	LF gain pot ACW
8	Keyway	41	LF gain pot CW
9	HF Freq pot wiper a	42	Filter Input
10	HF Freq pot wiper b	43	EQ Output
11	HMF Gain pot ACW	44	HF Filter pot ACW a
12	HMF Gain pot CW	45	HF Filter pot ACW b
13	HMF Gain pot wiper	—	HF Filter pot wiper a
—	HMF Freq pot CW a	46	HF Filter pot wiper b
14	HMF Freq pot wiper a	47	Filter Output
15	HMF Freq pot wiper b	48	EQ Input
16	HMF Freq pot CW b	49	0v
17	HMF Q pot wiper a	50	0v
18	HMF Q pot CW a	51	Spare
19	HMF Q pot ACW a	52	- 18v
20	HMF Q pot ACW b	53	+ 18v
21	HMF Q pot wiper b	54	LF Filter pot CW a
22	LMF gain pot ACW	55	LF Filter pot CW b
23	LMF gain pot CW	56	LF Filter pot wiper a
24	LMF gain pot wiper		
—	LMF Freq pot CW a		
25	LMF Freq pot wiper a		
26	LMF Freq pot wiper b		
27	LMF Freq pot CW b		
28	LMF Q pot wiper a		
29	LMF Q pot CW a		
30	LMF Q pot ACW a		
31	LMF Q pot ACW b		
32	LMF Q pot wiper b		
33	LF gain pot wiper		
—	LF Freq pot CW a		

SSL Stonesfield/SSL New York, LA, Milan

15 November 1985

To: SSL LA, NY, Milan cc CJ
 From: Trevor S

The 82E242 Rev. 4

There have been revisions to the basic design (the prototypes that you have in the demo console). The Q of the HF section on bell has been decreased from approx 1.7 to 1.0, to make something that is felt by most people to be less extreme and more generally useful (the Q on the '02 is approx 0.6). Frequency ranges and Q values of other sections are unchanged; to recap they are:-

HF	Amplitude	+/- 15dB Shelf) +/- 18dB Bell)	+/- 1dB
	Frequency	1.5KHz to 20KHz	+/- 10%
	Q (Bell)	1.0	+/- 10%
HMF	Amplitude	+/-15dB - lowest Q) +/-18dB - highest Q)	+/- 1dB
	Frequency	600Hz - 10KHz	+/- 10%
	Q	0.5 - 4.5, variable	+/- 15%
LMF	Amplitude	+/-15dB - lowest Q) +/-18dB - highest Q)	+/- 1dB
	Frequency	160Hz - 2.2KHz	+/- 10%
	Q	0.5 - 4.5, variable	+/- 15%
LF	Amplitude	+/-15dB - shelf) +/-18dB - bell)	+/- 1dB
	Frequency	40Hz -450Hz	+/- 10%
	Q (Bell)	1.7	+/- 10%

Component changes to bring old cards to this revision are:-

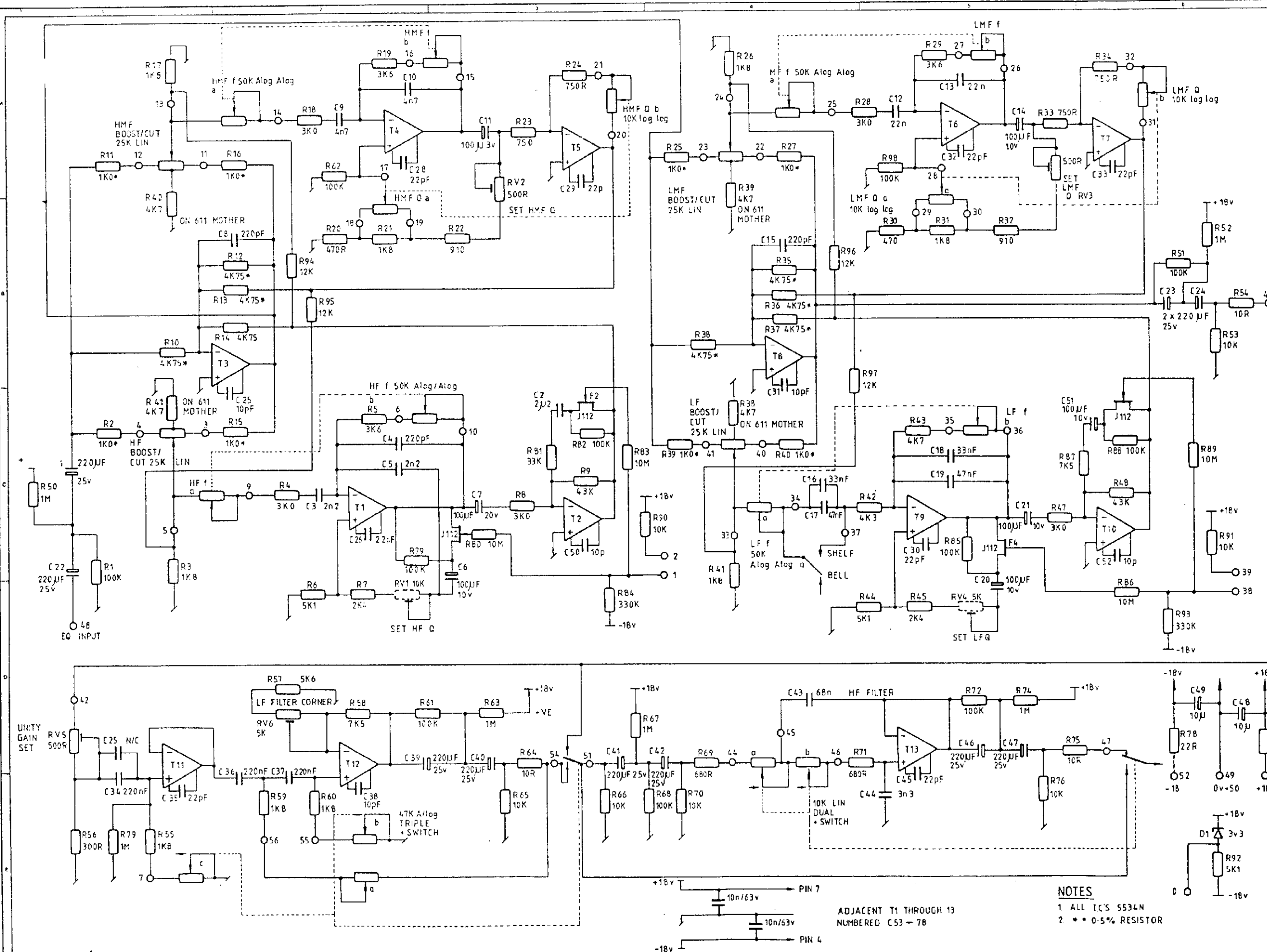
R6, 44	now	5K1
R7, 45	now	2K4
R81	now	33K
R87	now	7K5
R69,71	now	680R
RV1,4 (HF +LF Q pots)	now	10K

Set Up

Set Q pots for +18dB boost with frequency pots at mid position, and highest Q (MFs) or bell (HF, LF) selected.

PLEASE IMPLEMENT THESE MODS IN ORDER TO AVOID CONFUSION/MISREPRESENTATION.

Rev	Date	Chkd	Details
0			PROTOTYPE
1	3.85		REDRAWN VB
2	4.85		R7,9,45,48 CHANGED PINS 1 & 2 SWAPPED TR ON PIN 0 NOT S7
3	Nov 85	DM	FILTER VALUES CHANGED R67,44,45,69,71,81,87 RV1,4 AMENDED



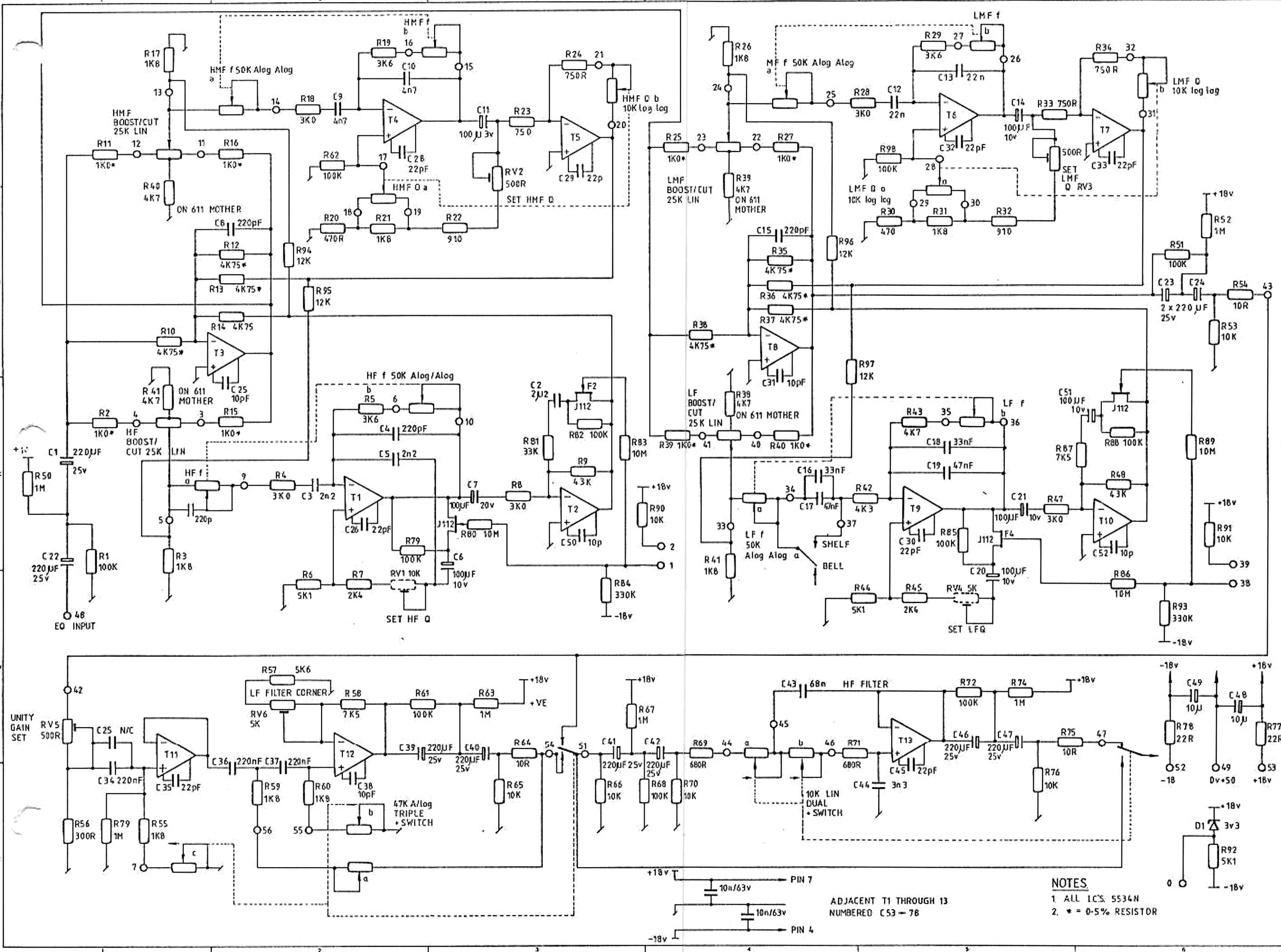
NOTES

1. ALL IC'S 5534N
2. * = 0.5% RESISTOR

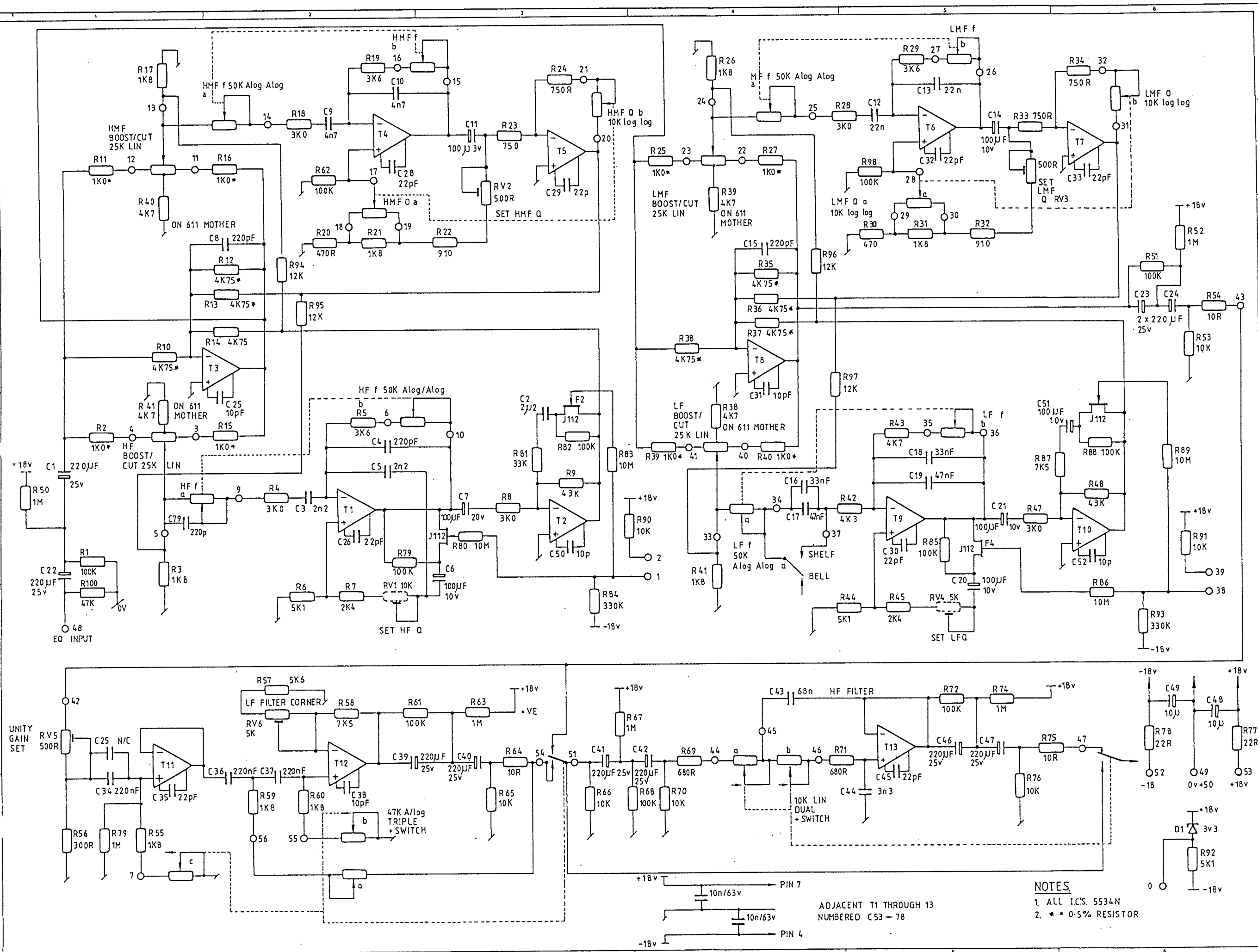
ADJACENT T1 THROUGH 13
NUMBERED C53 - 78

Title		ASSY REV N° 2
Drg. No.		T82242-70
Solid State Logic		1-20

Rev.	Date	Chkd	Details
0			PROTOTYPE
1	3.85		REDRAWN V
2	4.85		RT, 9, 45, 48 CHANG PINS 1 & 2 SWOPPE TR ON PIN 0 NOT 5
3	Nov 85	DM	FILTER VALUES CHANGED
4	Jan 86	CC	R6, 7, 44, 45, 69, 71, 81, 87 RV1, 4 AMENDED
			CAPACITOR ADDED ADJ. R3



- NOTES**
- 1 ALL IC'S 5534N
 - 2 * = 0.5% RESISTOR



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Rev.	Issue	Chkd	Details
P	a		PROTOTYPE
1	a	3-85	REDRAWN V.B.
1	b	3-85	R7,9,45,48 CHANGED PINS 1 & 2 SWAPPED TR ON PIN 0 NOT 57
2	a	4-85	FILTER VALUES CHANGED
3	a	Nov85	R81 TO 7K5 WAS 5K6
4	a	Nov85	NO CHANGE
5	a	DM Nov85	R6-7-44-45-69-71-81-87 RV 1-4 AMENDED EC092
5	b	GG Jan86	CAPACITOR ADDED ADJ R3
6	a	GG Jan86	CAP NUMBERED C79
7	a	DM Feb86	NO CHANGE.
8	a	DM June86	R100 ADDED

NOTES

1. ALL I.C.S. 5534N
2. * = 0.5% RESISTOR

Title
EQUALISER FILTERS.

Org. No.
T82242-70

Solid State Logic
2 2B